

Povyshenie effektivnosti modelirovaniya skhem silovoy elektroniki.: Na osnove ispolzovaniya algoritmov chislennogo integrirvaniya (Russian Edition)



Kniga prednaznachena dlya spetsialistov v oblasti modelirovaniya skhem silovoy elektroniki, studentov, izuchayushchikh pakety, baziruyushchiesya na yazyke PSpice i inzhenerov, proizvodyashchikh raschety skhem s pomoshchyu chislennykh metodov. Rezultaty, poluchennyye v nastoyashchem izdanii proshli sereznuyu aprobatsiyu v bolshom kolichestve skhem silovoy elektroniki. Avtor vyrazhaet glubokuyu blagodarnost kandidatu tekhnicheskikh nauk, dotsentu Bolotovskomu Yu.I. za znaniya, poluchennyye za vremya sovmestnoy raboty. Takzhe avtor s blagodarnostyu otmechaet svoikh kolleg po rabote Leonida Ilichy Gutina i Evgeniya Ivanovichy Vashkevicha, kotorye vnesli bolshoy vklad v reshenie konkretnykh zadach, nashedshikh svoe otrazhenie v knige.

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